

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Cancel claims 11-16 and 23.

1. (Previously Presented) A method for synchronizing the transfer of sequence numbers over a digital network, wherein an expected sequence number is compared to a received sequence number to determine if the received sequence number is acceptable, wherein a sequence number is acceptable if it is within a group of sequence numbers defined with respect to the expected sequence number, the method comprising:

    sending a first sequence number to a receiver, wherein the receiver includes an unknown expected sequence number;

    sending a second sequence number, wherein the first and second sequence numbers have values such that a subsequently sent starting sequence number is guaranteed to be accepted regardless of the value of the unknown expected sequence number; and

    sending the starting sequence number to cause resetting of the receiver to the starting sequence number.

2. (Previously Presented) The method of claim 1, wherein at least one of the sequence numbers is transferred with associated data.

3. (Original) The method of claim 2, wherein the sequence number and associated data include a packet.

4. (Original) The method of claim 1, wherein the sequence numbers have values within a predetermined range, wherein the range includes a minimum value and a maximum value.

5. (Original) The method of claim 4, wherein the first sequence number has a value that is approximately one-third of the maximum value in the range, and wherein the second sequence number has a value that is approximately two-thirds of the range.

6. (Currently Amended) The method of claim 5, wherein each sequence number is 16 bits, wherein the range is from 0 to 65535.

7. (Original) The method of claim 6, wherein the first sequence number has the value 21845 and wherein the second sequence number has the value 43690.

8. (Original) The method of claim 4, wherein the first sequence number has a value that is approximately one-half of the maximum value, and wherein the second sequence number has a value that is approximately the maximum value.

9. (Currently Amended) The method of claim 8, wherein each sequence number is 16 bits, wherein the range of each of the sequence numbers is from 0 to 65535.

10. (Original) The method of claim 9, wherein the first sequence number has a value of 32768 and wherein the second sequence number has a value of 65535.

11-16. (Canceled)

17. (Previously Presented) An apparatus for resynchronizing packets transferred in a digital network, wherein a packet includes a sequence number, the apparatus comprising at least one processor;

a computer-readable storage device including instructions executable by the at least one processor for:

sending a first packet sequence number to a receiver, wherein the receiver includes an unknown expected packet sequence number;

sending a second packet sequence number, wherein the first and second packet sequence numbers have values such that a subsequently sent starting packet sequence number is guaranteed to be accepted regardless of the value of the unknown expected packet sequence number; and

sending the starting packet sequence number to cause resetting of the receiver to the starting packet sequence number.

18. (Original) The apparatus of claim 17, wherein a maximum value for the packet sequence numbers is predefined, wherein the first packet sequence number has a value of approximately one-third of the maximum value and wherein the second packet sequence number has a value of approximately two-thirds of the maximum value.

19. (Original) The apparatus of claim 17, wherein a maximum value for the packet sequence numbers is predefined, wherein the first packet sequence number has a value of approximately one-half of the maximum value and wherein the second packet sequence number has a value of approximately the maximum value.

20. (Previously Presented) A computer-readable storage device including instructions executable by a processor for resynchronizing packets transferred in a digital network, wherein a packet includes a sequence number, the computer-readable storage device comprising:

sending a first packet sequence number to a receiver, wherein the receiver includes an unknown expected packet sequence number;

sending a second packet sequence number, wherein the first and second packet sequence numbers have values such that a subsequently sent starting packet sequence number is guaranteed to be accepted regardless of the value of the unknown expected packet sequence number; and

sending the starting packet sequence number to cause resetting of the receiver to the starting packet sequence number.

21. (Previously Presented) The computer-readable storage device of claim 20, wherein a maximum value for the packet sequence numbers is predefined, wherein the first packet sequence number has a value of approximately one-third of the maximum value and wherein the second packet sequence number has a value of approximately two-thirds of the maximum value.

22. (Previously Presented) The computer-readable storage device of claim 20, wherein a maximum value for the packet sequence numbers is predefined, wherein the first packet sequence number has a value of approximately one-half of the maximum value and wherein the second packet sequence number has a value of approximately the maximum value.

23. (Canceled)